| Name | Date | |
|------|----------|--|
| | | |

Part 1; Q1-6 _____ of 102

Part 2; Q 7-10 _____ of 87

Digital Electronics Exam 1

1. Convert the following to binary. (2 points each)

19 _____ 12 ____ 6 ____ 16 ____

2. Convert the following to decimal. (2 points each)

11011

1101 _____

11000 _____ 11010 ____

3. Complete each of the items in the table. (5 points each)

| 9 | AND | OR | NOT |
|----------------------------|------------------------|------------------------|----------------------|
| Switch Based Circuit | | | |
| Boolean Expression | | | indektor - Arion roa |
| To division in the same | INPUTS OUTPUT A B L | INPUTS OUTPUT A B L | INPUT OUTPUT A L |
| Truth Table | | | |
| The state of | | Loss Bright | |
| Symbol | | | |
| | | - | 1 628.3 |

| 4. | Alongside each acronym, place the words that each stands for. (2 points each) | |
|----|---|---|
| | NC | _ |
| | CMOS | _ |
| | TTL | |
| | IC | |
| | | |

5. Number the binary digits on the set of lines immediately beneath each of the binary digits. On the line beneath that, indicate the place value of that digit.

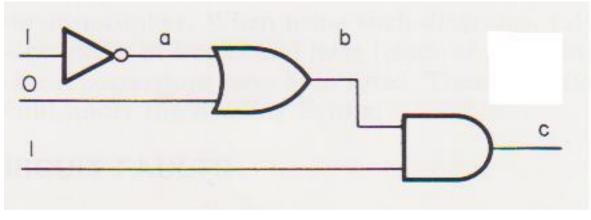
(5 points each part, 10 points total)

| 1 | U | 1 | 1 | U | U | 1 | 1 | 0 | 1 |
|---|---|---|---|---|---|---|---|---|---|
| 1 | Λ | 1 | 1 | Λ | Λ | 1 | 1 | • | 1 |

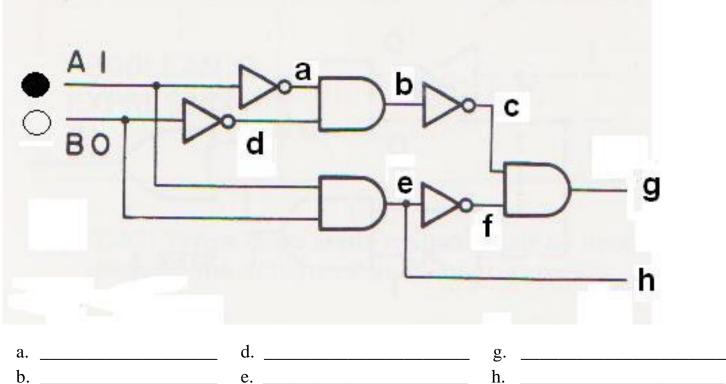
6 Record the output that will occur at points a, b and c given the inputs shown.

(2 points each)

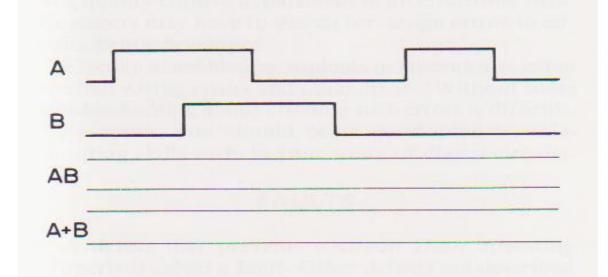
LSB



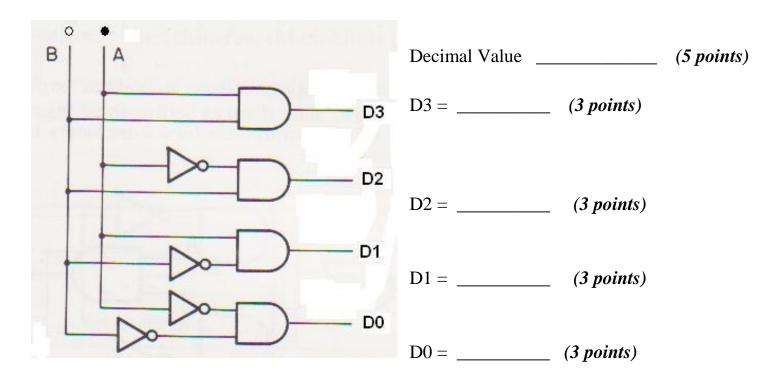
7. Fill in the value that will be present at each of the points indicated. (5 points each)



(10 points each)



Construct the output graph for the AND circuit represented by "AB." Then construct the output graph for the OR circuit represented by "A + B." 9. Record the output for D0, D1, D2, and D3. What decimal value is represented by these binary digits?



10. Write the Boolean expressions for the logic diagram shown in problem 7. (5 points each)